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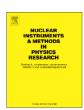
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Detailed studies of full-size ATLAS12 sensors

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ABSTRACT

The "ATLAS ITk Strip Sensor Collaboration" R&D group has developed a second iteration of single-sided n⁺-in-p type micro-strip sensors for use in the tracker upgrade of the ATLAS experiment at the High-Luminosity (HL) LHC. The full size sensors measure approximately $97 \times 97 \text{ mm}^2$ and are designed for tolerance against the $1.1 \times 10^{15} \, n_{eq}/cm^2$ fluence expected at the HL-LHC. Each sensor has 4 columns of 1280 individual 23.9 mm long channels, arranged at 74.5 μm pitch. Four batches comprising 120 sensors produced by Hamamatsu Photonics were evaluated for their mechanical, and electrical bulk and strip characteristics. Optical microscopy measurements were performed to obtain the sensor surface profile. Leakage current and bulk capacitance properties were measured for each individual sensor. For sample strips across the sensor batches, the inter-strip capacitance and resistance as well as properties of the punch-through protection structure were measured. A multi-channel probecard was used to measure leakage current, coupling capacitance and bias resistance for each individual channel of 100 sensors in three batches. The compiled results for 120 unirradiated sensors are presented in this paper, including summary results for almost 500,000 strips probed. Results on the reverse bias voltage dependence of various parameters and frequency dependence of tested capacitances are included for validation of the experimental methods used. Comparing results with specified values, almost all sensors fall well within specification.

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1. Introduction

The foreseen upgrade of the Large Hadron Collider (LHC) to the High-Luminosity LHC (HL-LHC) is scheduled to deliver collisions in 2022 [1]. To achieve a total cross-section of $3000 \, \mathrm{fb}^{-1}$, the instantaneous luminosity of the HL-LHC is expected to reach $5 \times$ 10^{34} cm⁻² s⁻¹ at a centre of mass energy of 14 TeV. The increase in particle fluence necessitates an upgrade of the ATLAS inner detector: an all-silicon new inner tracker (ITk) [2] is proposed to replace the current SemiConductor Tracker (SCT) and Transition Radiation Tracker (TRT). The ITk layout as presented in the Letter of Intent [3] assumes silicon microstrip detectors to be used for 7 endcap disks, and 5 barrel layers. From simulations verified by experiments, the highest particle fluence in the barrel short strip layer is expected to be $5.3 \times 10^{14} \, n_{eq}/cm^2$ [4]. Including a safety factor of 2, candidate ITk sensors will have to be radiation hard up to levels of $1.1 \times 10^{15} \, n_{eq}/cm^2$. The goal of the ITk Strip Sensor collaboration is to develop a silicon microstrip sensor that is suitable for use in the new ITk. Results of detailed studies of properties of full-size sensor prototypes are presented in this paper, whereas studies of radiation damage of 1 × 1 cm² miniature sensors are reported in [5,6].

2. ATLAS12 large area sensors

The ATLAS12 sensors are the second iteration of sensors designed for the Upgrade ITk, superseding the ATLAS07 types [7]. To cope with the effects of radiation damage during the sensor lifetime, operation in partial depleted mode is foreseen towards the end of the detector lifetime. The sensor will need specially designed structures between the strips to guarantee strip isolation during its lifetime and mitigate radiation-induced surface damage whilst retaining a low inter-strip capacitance. A single-sided n⁺-in-p sensor offers a trade-off between the above requirements and cost [8]. 120 full-size prototype sensors were manufactured by Hamamatsu Photonics [9] on a 6 in. wafer process using p-type float zone silicon. Sensors were delivered in 4 batches: VPX12318, VPX12518, and VPX12519, of 33, 32 and 35 sensors respectively were shipped to the UK, and VPX14757 containing 20 sensors was delivered to the US.

The largest square sensor that can be cut from the wafer measures $97.5 \times 97.5 \text{ mm}^2$. This geometry is denoted "Outer Cut". Sensors with $462 \, \mu m$ reduced edge metal all around, denoted

"Inner Cut", were made available as well to allow for smaller inactive regions in the final detector layout. The sensor strip implants are arranged in 4 columns of 1282 implants each, with the top and bottom implant of each column serving as field shaping strips. The resulting strip pitch is 74.5 µm and the strip length is 23.9 mm. All strips are connected to the bias rail by polysilicon resistors implanted in the sensor. For biasing the central two columns, a bias rail runs through the centre of the sensor. Top metal layer strips are AC-coupled to the strip implants, which have p-stop traces running in between along the full length for strip isolation. The top metal layer is passivated, with openings for probing and wirebonding. Fig. 1 contains a photograph showing the sensor details.

3. ATLAS12A mechanical properties

The sensor mechanical specifications state the following:

- nominal thickness: $310 \pm 20 \mu m$;
- thickness variation: $\pm\,10\,\mu m$ across the sensor. This means some thickness variation is allowed between sensors, but not across a sensor;
- sensor flatness when unstressed: $< 200 \mu m$;
- outer cut dimensions: $97540 \pm 25 \mu m^2$;
- inner cut dimensions: $95,692 \pm 25 \mu m^2$; and
- no cracks or chips at the dicing line to extend further inwards than 50 μm .

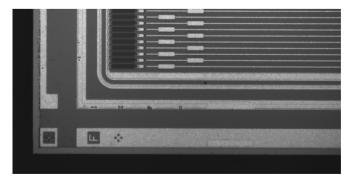


Fig. 1. Picture of the corner of an Outer Cut ATLAS12 sensor, illustrating the layout of the bias rail, guard ring, strip top metal and bias resistors. The dicing streets intended for the Inner Cut sensors are clearly visible.

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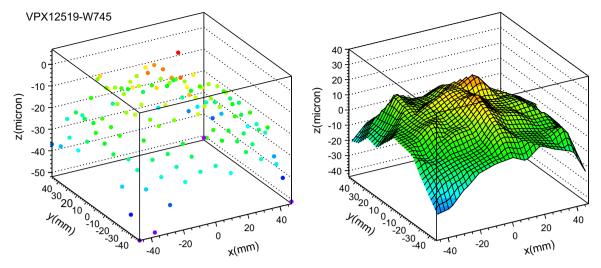


Fig. 2. Measured space points (left) and net bow interpolated plot (right) for sensor VPX12519-W745.

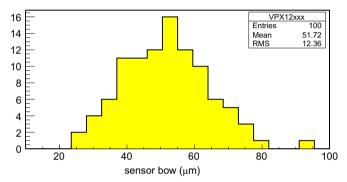


Fig. 3. Histogram of the maximum bow of the sensors from batches VPX12318, VPX12518 and VPX12519.

For visual inspection and evaluation of the above properties, a non-contact optical Coordinate Measurement Machine (CMM) was used, with a precision better than $2 \mu m$ in x, y, and $4 \mu m$ in z. A coordinate system is defined, with the origin (0, 0, 0) at the centre of the sensor, with the axes in right-handed orientation with the zaxis pointing upwards, and the scratch pad located in the bottom right. For measuring outer and inner cut sensors within a reasonable timespan, measurements were taken on a 11×11 grid with 9.440 mm spacing. To compensate for tilt of the freely suspended sensor, a flat plane is fitted to, and subtracted from the space points measured. The measurement points and an interpolated net bow result for a typical sensor are plotted in Fig. 2. The difference between the highest and lowest points from the net bow is taken as the maximum bow. A histogram of the maximum bow of sensors from batches VPX12318, VPX12518 and VPX12519 is plotted in Fig. 3, the resulting average is $51.7 \mu m$, with an RMS deviation of 12.4 μ m, which is well within specification.

4. Bias voltage and frequency dependence of parameters

4.1. Leakage current

Development of the leakage current against bias voltage ($V_{\rm bias}$) for all 120 sensors is plotted in Fig. 4. The data was taken at $T=21~^{\circ}{\rm C}$, and at a relative humidity (RH) of $40\pm10\%$. Since the ATLAS12 sensors are very sensitive to the ambient humidity, the initial batch VPX12318 was tested in a dry N₂ environment. As can be observed from the graphs on VPX12318 however, a

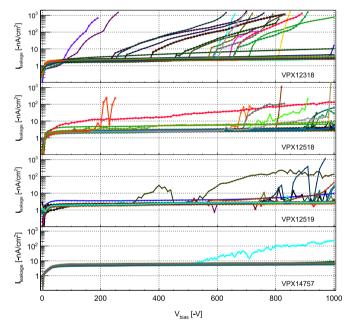


Fig. 4. Leakage currents versus $V_{\rm bias}$ for ATLAS12 sensors at 21 $^{\circ}{\rm C},$ grouped per batch.

considerable number of sensors exhibit early, soft breakdown. The two subsequent batches were stored in dry atmosphere, and measured in standard cleanroom conditions at $T=21\,^{\circ}\text{C}$, and $40\pm10\%$ RH, and fewer soft breakdowns occur. Measurements were carried out using Keithley 2410 and 6517 Source Measure Units (SMUs), with 10 V step increases with a 10 s delay from 0 V to $-1000\,\text{V}$ unless the compliance limit of $100\,\mu\text{A}$ was exceeded. In some cases, the sensors were trained by increasing the bias voltage very slowly, after which a normal IV measurement was recorded

With a $2~\mu\text{A/cm}^2$ specification for leakage current, and the area inside the bias ring spanning 91.8 cm², the maximum allowed leakage current at -600~V bias and $T=21~^\circ\text{C}$, is $184~\mu\text{A}$. Two sensors exhibited early breakdown, and were excluded from all further measurements. A histogram containing the leakage currents at $V_{\text{bias}}=-600~\text{V}$ of the remaining 118 sensors is plotted in Fig. 5. It shows that the sensors examined sit well below $1~\mu\text{A}~\text{cm}^2$, comfortably within specifications.

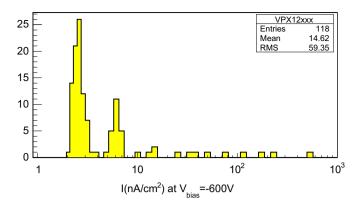


Fig. 5. Histogram of the normalised leakage current at $V_{\text{bias}} = -600 \, \text{V}$, at $T = 21 \, ^{\circ}\text{C}$.

4.2. Bulk capacitance

The sensor bulk capacitance ($C_{\rm bulk}$) was measured as a function of bias voltage to determine the sensor depletion, and versus frequency to verify the validity of the measurement. Using a Wayne-Kerr 6440B LCR meter in R-C series network mode, the frequency dependence of $C_{\rm bulk}$ for various $V_{\rm bias}$ values of sensor VPX12519-W748 is plotted in Fig. 6. As can be observed, $C_{\rm bulk}$ is constant between 0.5 and 6 kHz for a wide range of $V_{\rm bias}$. $C_{\rm bulk}$ measurements were carried out with frequencies between 1 and 2 kHz.

Fig. 7 contains plots of $1/C_{\rm bulk}^2$ versus $V_{\rm bias}$ for 33 sensors of batch VPX12519. As can be seen from the plot, behaviour is very consistent within the batch. The full depletion voltage ($V_{\rm FD}$) is determined by taking the intersection point of straight line fits to the sloped, and flat region. Histogrammed $V_{\rm FD}$ for 98 sensors of batches VPX12318, VPX12518 and VPX12519 are plotted in Fig. 8: the distribution is narrow, with an average of $V_{\rm FD} = 365$ V, and a RMS of 8.6 V.

Assuming a parallel plate capacitor model for the sensor, with the area defined as the inside of the bias ring, the active thickness is estimated at 301 μ m. With the area and thickness known, the doping concentration can be calculated both from the sloped region and the saturated region of the $C_{\rm bulk}$ data. For both methods, the calculated doping concentrations are plotted with blue and red lines respectively in Fig. 9. The resulting values of $5.3 \pm 0.12 \times 10^{12} \, {\rm cm}^{-3}$ are consistent with a wafer bulk resistivity of $2.5 \, {\rm k}\Omega$ cm, which is, although $V_{\rm FD}$ is higher than the desired value of $< 300 \, {\rm V}$, in accordance with specification.

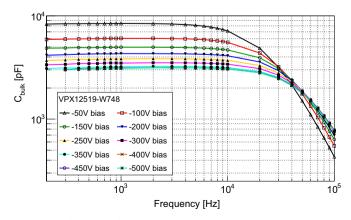


Fig. 6. Plot of C_{bulk} versus the measurement frequency.

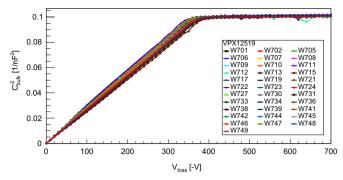


Fig. 7. Plot of $1/C_{\text{bulk}}^2$ versus V_{bias} for the sensors of batch VPX12519.

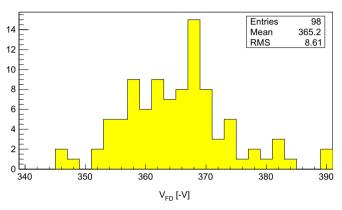


Fig. 8. Histogram of $V_{\rm FD}$ determined from $C_{\rm bulk}$, see text.

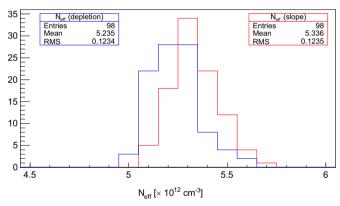
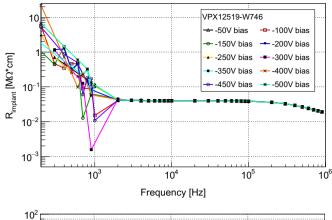


Fig. 9. Histogrammed doping concentrations, $N_{\rm eff}$, extracted from the data plotted in Fig. 7. The blue line histogram is extracted from the flat region and the red line histogram is from the sloped region. (For interpretation of the references to colour in this figure caption, the reader is referred to the web version of this paper.)

4.3. Strip coupling capacitance

The capacitance and resistance of the R–C network formed by the strip implant resistance ($R_{\rm implant}$) and the capacitance between the implant and the top metal readout strip ($C_{\rm coupling}$) are plotted as a function of measurement frequency for various bias voltages in Fig. 10. It has been measured directly between the strip implant and top metal using an LCR in R–C series network mode. As can be seen from the plots, the behaviour of the capacitance and implant resistance both show little variation across a 2–100 kHz range. At higher frequencies, the resistance and capacitance drop, as expected from the implant resistance and the transmission line formed between the strip implant and top metal. The nominal value of $C_{\rm coupling}$ is 25.7 pF/cm, which is well within the specification of > 20 pF/cm. More results are presented and discussed in Section 5.

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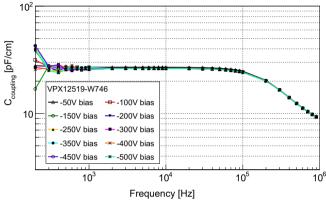


Fig. 10. Frequency dependence of R_{implant} (top plot), and C_{coupling} (bottom plot), for various V_{bias} values.

4.4. Bias resistance

The bias resistance ($R_{\rm bias}$) for a small sample of channels was measured directly, as part of the DC measurement of the Punch-Through Protection circuit behaviour, see Section 4.7. The current between the implant and the bias rail was monitored while the voltage was increased from 0 V to 10 V in 0.25 V steps. The results lie within 1.5 ± 0.03 M Ω , which is well within the specification of $R_{\rm bias} = 1.5 \pm 0.5$ M Ω . More results on many individual strips probed are presented and discussed in Section 5.

4.5. Inter-strip capacitance

The inter-strip capacitance ($C_{\rm is}$) dominates the input capacitance contributions to the front-end electronics, and determines the noise levels of the detector. Its value is one of the main input parameters to the design process of new revisions of the front-end preamplifier, and should be kept as low as possible. The specification states that it should be < 0.8 pF/cm, measured at 100 kHz with both neighbouring strips grounded. For ATLAS12 sensors with 23.9 mm strip length, $C_{\rm is}$ < 1.91 pF. The development of $C_{\rm is}$ versus $V_{\rm bias}$ for 4 sample channels is plotted in Fig. 11. The $C_{\rm is}$ value is largely determined by the strip metal geometry, and sensor surface layout. Its frequency dependence is plotted in Fig. 12, results suggesting that the measurement frequency should lie in the 50 kHz to 1 MHz range. The average value measured is 0.77 pF/cm, confirming findings from other institutes.

4.6. Inter-strip resistance

The inter-strip resistance (R_{is}) was measured by applying a master voltage (V_M) to a strip implant, and measuring the induced current (i_c) on the neighbouring strip by sampling the voltage

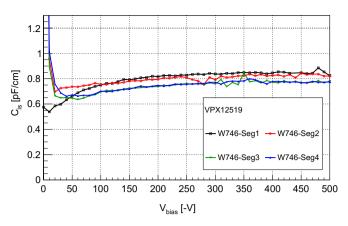


Fig. 11. Inter-strip capacitance C_{is} result for a few sample strips versus V_{bias} .

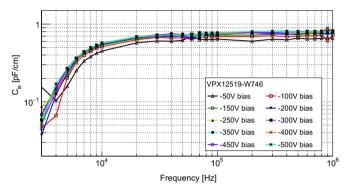


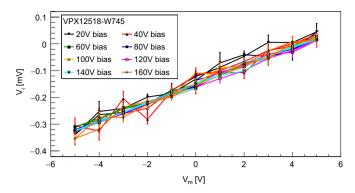
Fig. 12. Behaviour of the inter-strip capacitance C_{is} versus measurement frequency for various V_{bias} values.

developed across its bias resistor $V_{\rm S}$. $V_{\rm M}$ is applied in 1 V steps in the interval [-5 V, +5 V]. Even though instruments with high input impedance and excellent isolation were used (Keithley 6517, Keithley 2000), it is likely that above 10–20 G Ω the accuracy is limited due to setup systematics. Nonetheless, from the plot of $V_{\rm S}$ against $V_{\rm M}$ in the top plot in Fig. 13, it shows that there is a clear linear dependence of the two properties. The bottom plot contains summary results on extracted $R_{\rm is}$ for sample strips for a range of $V_{\rm bias}$.

4.7. Punch-through protection

To protect the strip implants and front-end electronics from excessive charge liberated in the bulk, for example as the result of a beam splash, a Punch-Through Protection (PTP) structure is incorporated in the sensor. It embodies an elongation of the strip implant under the bias resistor, leaving a 8 μ m gap to the bias rail. Soft breakdown will occur across this gap as soon as a threshold potential is exceeded. The PTP structure is of the "fully gated" design, where the bias rail implant extends over the gap, covering the PTP gap entirely, see [5].

A static measurement to determine the threshold voltage consists of monitoring the current whilst increasing the voltage imposed on the strip implant. To limit the power dissipation, voltage and current are limited to 50 V and 50 μ A respectively. Typical results for sample channels of the differentiated dV/dI plot showing the resistance against applied voltage are plotted in Fig. 14. A threshold at 13 V is clearly visible, with the apparent resistance dropping sharply from the 1.5 M Ω bias resistance to tens of $k\Omega$. As expected, the sharper drops are observed when the voltage is applied on the implant end nearer to the PTP structure.



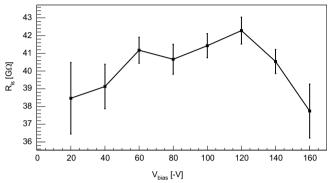


Fig. 13. Top plot: measured voltage induced by i_c against the applied voltage V_M for various values of $V_{\rm bias}$ (see text). Bottom plot: extracted $R_{\rm is}$ against bias voltage.

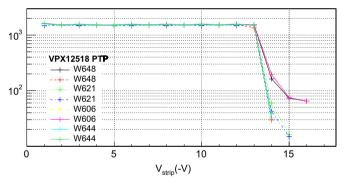


Fig. 14. Plot showing the apparent resistance $\Delta V/\Delta I$ against the voltage applied to the strip implant $V_{\rm strip}$.

5. Strip scan results

5.1. Measurement techniques

To evaluate properties of individual strips, study the uniformity of electrical characteristics over the entire sensor surface, and compare against other sensors, full scans of individual sensor channels were made. A relatively simple Strip Test protocol, see Section 5.2, was used to check for strip shorts and pinholes, and measure the $C_{\rm coupling}$ and $R_{\rm bias}$ for each individual strip. A single probe needle was used for probing the initial 31 sensors in batch VPX12318, after that a custom 32 channel probecard and associated multiplexing equipment were used, reducing the sensor measurement time from 14 to under 3 h.

During the test, the sensor was held in place by a low-force spring rather than a vacuum jig to minimise stress and strain on the sensor that could lead to early breakdown or influence strip test results, with the bias provided by wirebonded contacts to the edge metal and bias rail. Additional needles on the probecard provide a edge-sense feature, breaking a contact as soon as the needles touch the sensor surface. After detecting touch-down, the

z-height is recorded and a pre-defined probestation chuck overdrive is applied to ensure a good contact between the probecard needles and the sensor. This procedure was repeated for every touchdown, providing uniform and consistent contact quality across the entire sensor. In contrast, for the single-needle measurement such functionality does not exist, resulting in variable contact quality due to the non-planarity of the prober chuck, jig or sensor.

5.2. Strip test description

A "Full Strip Test" refers to the following sequence for testing a single strip on a sensor that is biased at -150 V to achieve full strip isolation:

- Connect to the strip metal using a probe needle.
- Connect an SMU set to 10 V to the strip metal through a 1 MΩ current-limiting series resistance, and measure the current.
- If the current exceeds 1 μ A, the strip is marked as having a short, and the measurement proceeds to the next strip.
- The SMU voltage is increased to 100 V, and the current is recorded after a short time delay.
- The strip is connected to GND, and subsequently to the LCR meter.
- The LCR meter measures the R-C series network formed by R_{bias} and C_{coupling}, and results are recorded.
- The strip metal is connected to GND and the measurement proceeds to the next strip.

5.3. Strip test results summary

Histograms of accumulated data of strip metal current (top plot), $C_{\rm coupling}$ (middle plot), and $R_{\rm bias}$ (bottom plot) are plotted in Fig. 15. Data from almost 500,000 channels from 97 sensors from batches VPX12318, VPX12518, and VPX12519 is included. One sensor exhibited high leakage current and was excluded from the measurement. Ignoring defective channels, the measured values fall well within specification, see Table 1.

A geometrical map of accumulated defective channels is displayed in Fig. 16. There seems to be no obvious correlation with sensor geography, although segment 3 seems to have significantly more defects than the other segments. Of the 132 recorded faulty channels across 97 sensors probed, 79 have a short or pinhole, 10 fail the C_{coupling} requirement, and of 43 strips the R_{bias} is out of range. The total fraction of good strips is 99.97%, comfortably exceeding the 98% requirement.

6. Summary and conclusion

Detailed studies on ATLAS12 sensors produced by Hamamatsu Photonics have been presented in this paper. Results from the evaluation are summarised in Table 1, and compared to the specifications set out in the ATLAS12 Technical Specification Document. 118 of 120 sensors tested satisfy the specifications for non-irradiated sensors for maximum bow, leakage current. Only 0.03% of probed strips were measured as defective; all other strips satisfied the requirements for $C_{\rm coupling}$ and $R_{\rm bias}$. The values of the Inter-Strip Capacitance and Resistance measured for sample channels are well within specification. The conclusion of examination of 120 sensors is that they are of high quality, have good consistency between sensors in three different batches, and excellent uniformity across channels on a sensor. They satisfy the requirements, and are considered suitable for use in the barrel of the ATLAS Upgrade ITk.

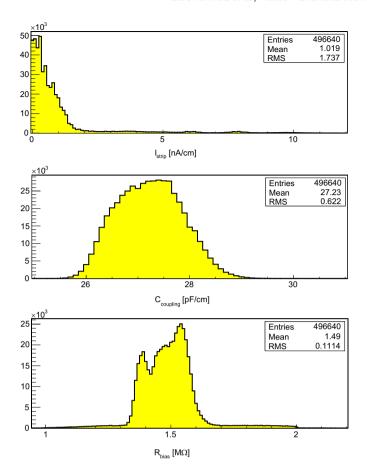


Fig. 15. Accumulated strip test results for 97 sensors from batches VPX12318, VPX12518, and VPX12519.

Table 1Comparison of ATLAS12 specification and summarised measurement values for various sensor properties.

Sensor property	Specification	Measured values
Maximum bow Leakage current at V _{bias} = 600 V	$<200~\mu m \\ <2~\mu A/cm^2$	$51.7 \pm 12.4 \mu\text{m}$ < 1 $\mu\text{A/cm}^2$ for 118 of 120 sensors
Depletion voltage	< 300 V, wafer resistivity allowing	$365 \pm 8.6 \text{ V}$
Bias resistor value	$1.5\pm0.5~\mathrm{M}\Omega$	$1.49 \pm 0.111~\text{M}\Omega$
Coupling capacitance	$>$ 20 pF/cm at $V_{\text{bias}} = 300 \text{ V}$	$27.2\pm0.62~pF/cm$
Inter-strip capacitance Inter-strip resistance Percentage good strips	< 0.8 pF/cm < 0.8 pF/cm $> 15 \pm 5 \text{ M}\Omega$ > 98% per segment	$0.77 \text{ pF/cm} > 30 \text{ G}\Omega > 99.97$

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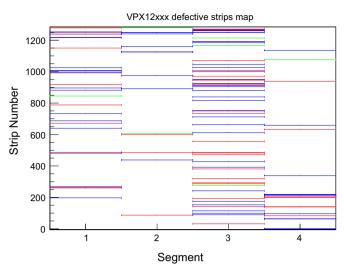


Fig. 16. Map of 132 faulty strips accumulated from 97 measured sensors. Strips failing from R_{bias} , C_{coupling} , and strip current are plotted in red, green and blue respectively. (For interpretation of the references to colour in this figure caption, the reader is referred to the web version of this paper.)

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